



09881472 Corr

Patent 6,788,614

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,788,614 B2

Docket No: 303.729US1

Issue Date: September 7, 2004

Patentee: Ramandeep S. Sawhney

Customer No.: 21186

Confirmation No.: 7503

Title SEMICONDUCTOR MEMORY WITH WORDLINE TIMING

REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Certificate  
DEC 14 2004

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

**As the error is that of the Patent Office, it is believed that no fee is due.**

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743

Respectfully Submitted

RAMANDEEP S. SAWHNEY

By his Representatives,

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1 Dec '04

By:

Timothy B. Clise  
Reg. No: 40,957  
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Michael M Linse  
Name

Signature

DEC 15 2004



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ramandeep S. Sawhney

Examiner: Pham, Ly

Patent No.: 6,788,614

Group Art Unit: 2818

Issue Date: September 7, 2004

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Title: SEMICONDUCTOR MEMORY WITH WORDLINE TIMING

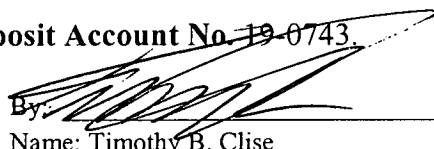
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We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer No: 21186

By:   
Name: Timothy B. Clise  
Reg. No. 40,957  
TBC:CMG: mml

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Michael M Linse  
Name

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,788,614 *B2*  
 DATED : September 7, 2004  
 INVENTOR(S) : Sawhney

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 13, in Claim 1, after "signal" delete ",".

In column 10, line 18, in Claim 1, after "decoder" delete ",".

In column 10, line 37, in Claim 4, after "a" insert - - second - -.

In column 10, line 42, in Claim 4, after "adapted to " delete "selectively".

In column 10, lines 43 - 44, in Claim 4, delete "second address decoder based on the second input signal and the" before "first address".

In column 10, line 55, in Claim 7, after "first memory array" delete ",".

In column 10, line 57 - 60, in Claim 7, delete "a first input adapted to receive at least one first input signal, the first input signal including a sense an isolation signal connected to a sense amplifier for the second memory array;".

In column 11, line 1, in Claim 7, delete "**adapter**" and insert - - **adapted** - -, therefor.

In column 11, line 3, in Claim 7, delete "**isolated**" and insert - - **isolation** - -, therefor.

In column 11, line 16, in Claim 11, after "wherein the" insert - - first - -.

In column 11, line 26, in Claim 13, delete "gaze" and insert - - **gate** - -, therefor.

In column 11, line 29, in Claim 13, after "system" insert - - , - -.

In column 12, line 2, in Claim 13, after "input" insert - - , - -.

In column 12, line 19, in Claim 19, after "claim 13" insert - - , - -.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,788,614

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